

FIG. 1 (Prior Art)

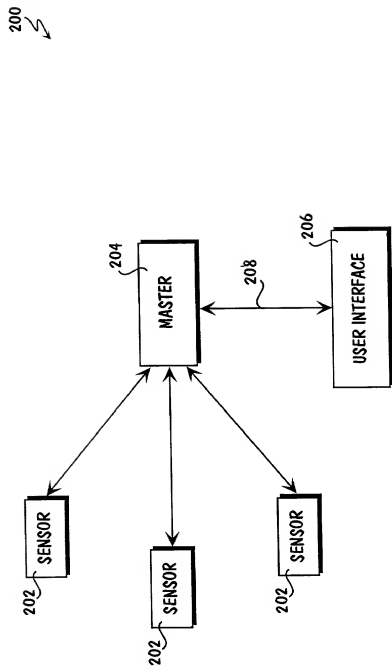


FIG. 2 (Prior Art)

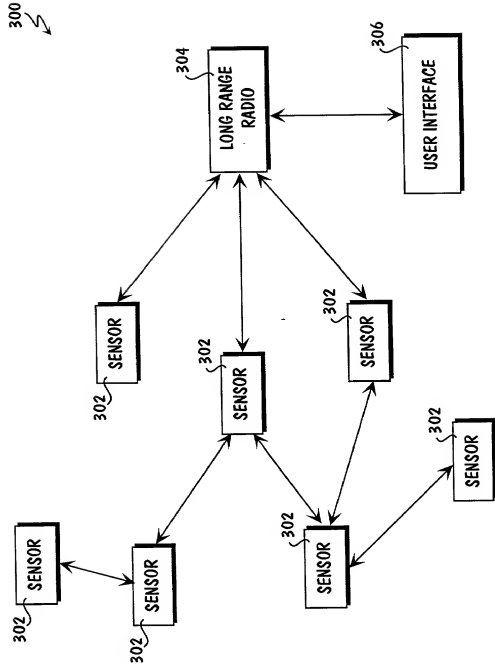


FIG. 3 (Prior Art)

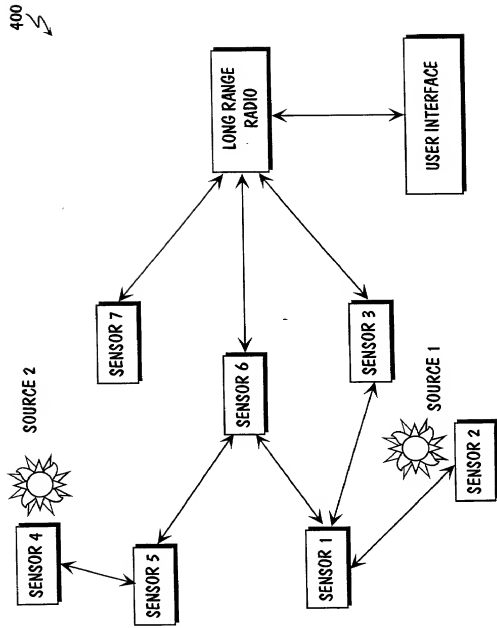


FIG. 4 (Prior Art)

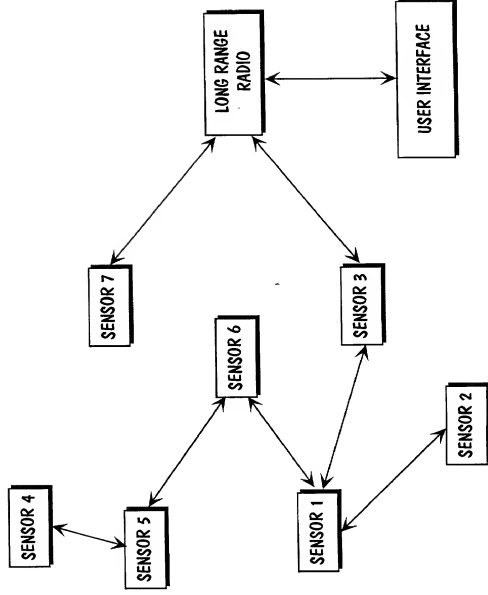


FIG. 5 (Prior Art)

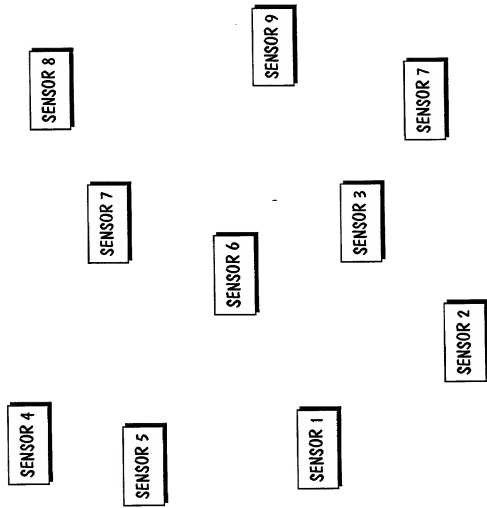


FIG. 6 (Prior Art)

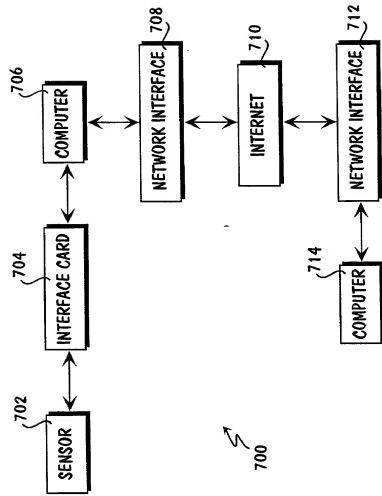


FIG. 7 (Prior Art)

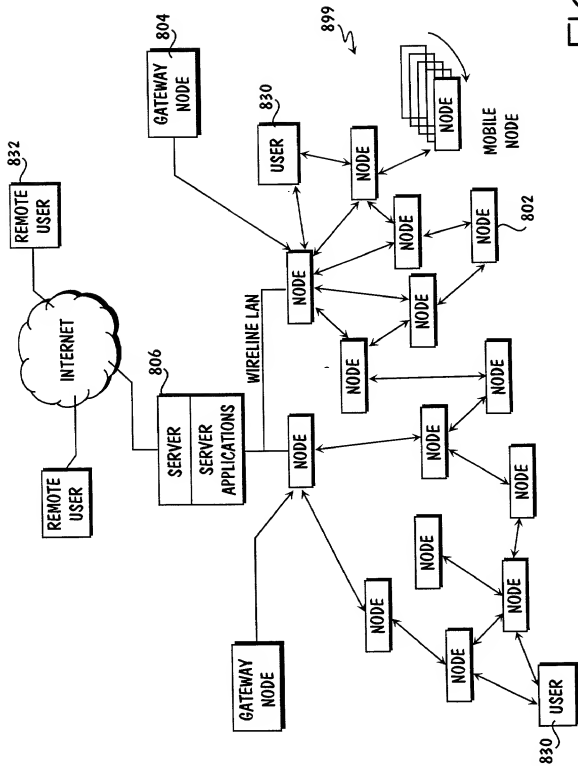


FIG. 8



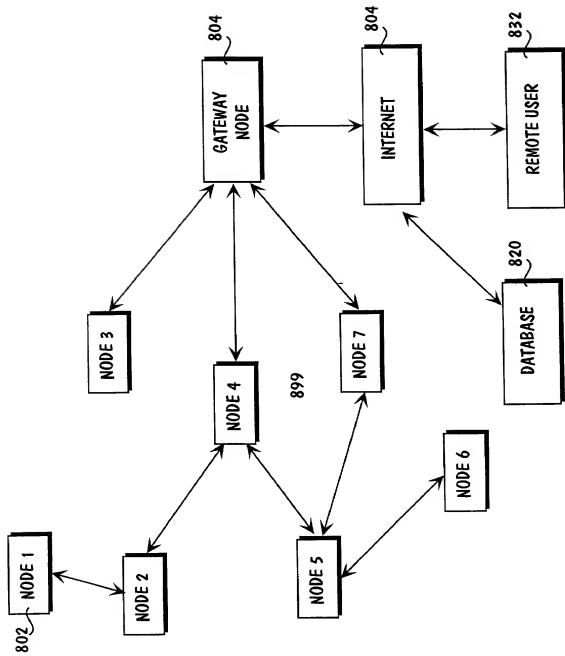


FIG. 9

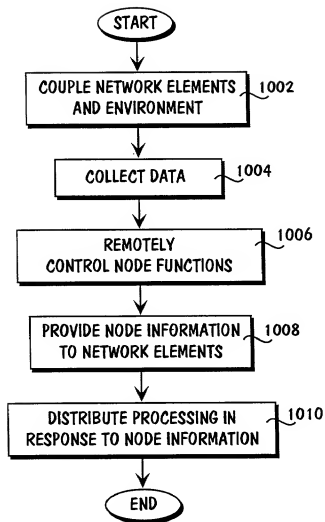
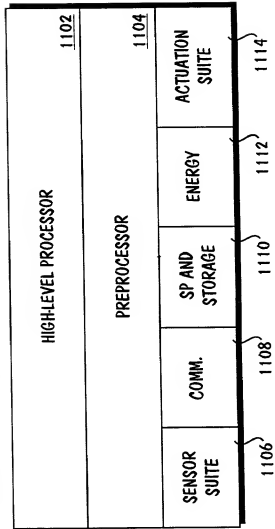


FIG. 10

PROCESSING WITHIN NODES



1100

FIG. 11

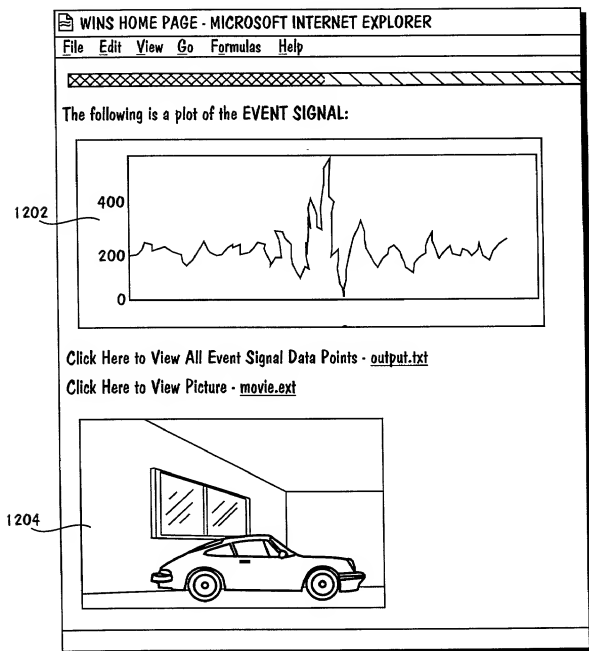


FIG. 12

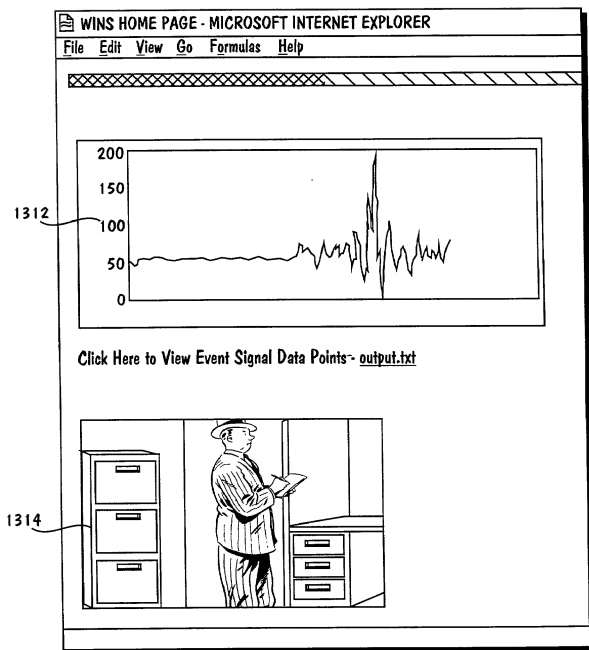
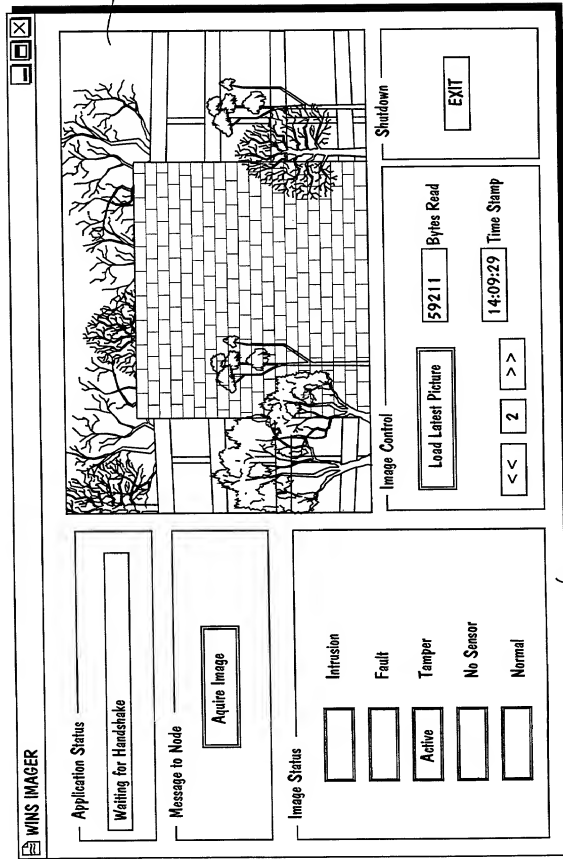


FIG. 13

1400



1404

FIG. 14

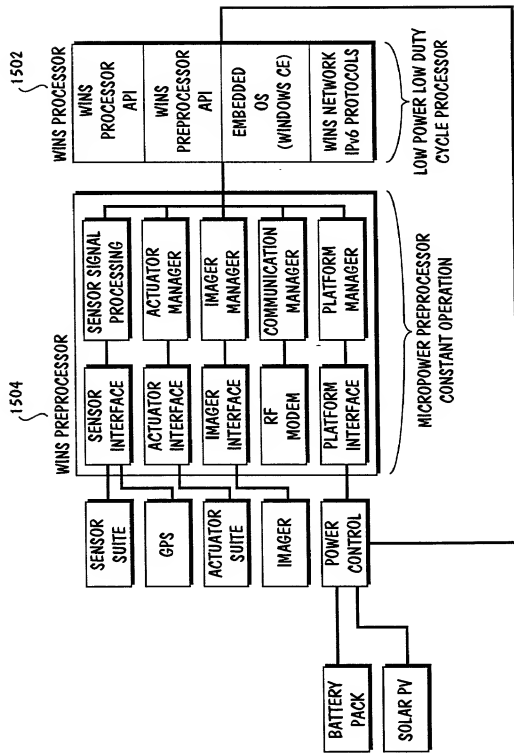


FIG. 15

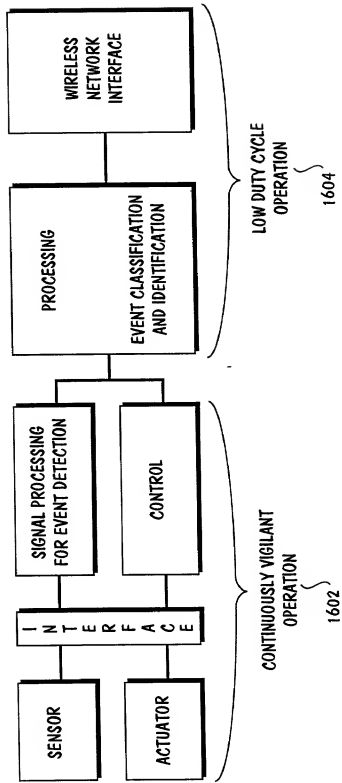


FIG. 16



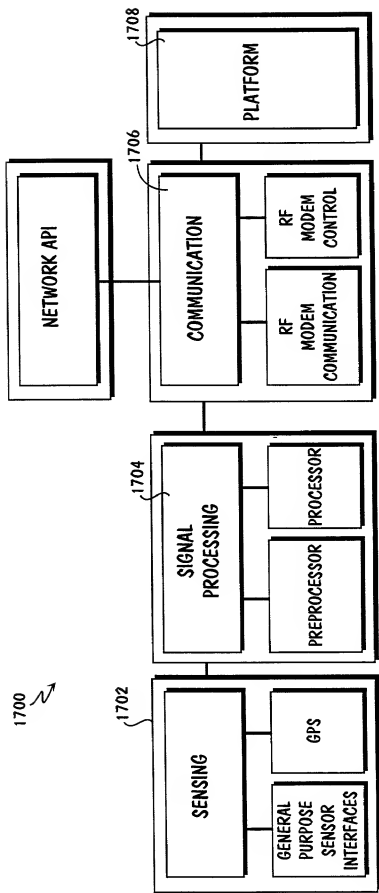


FIG. 17

1800

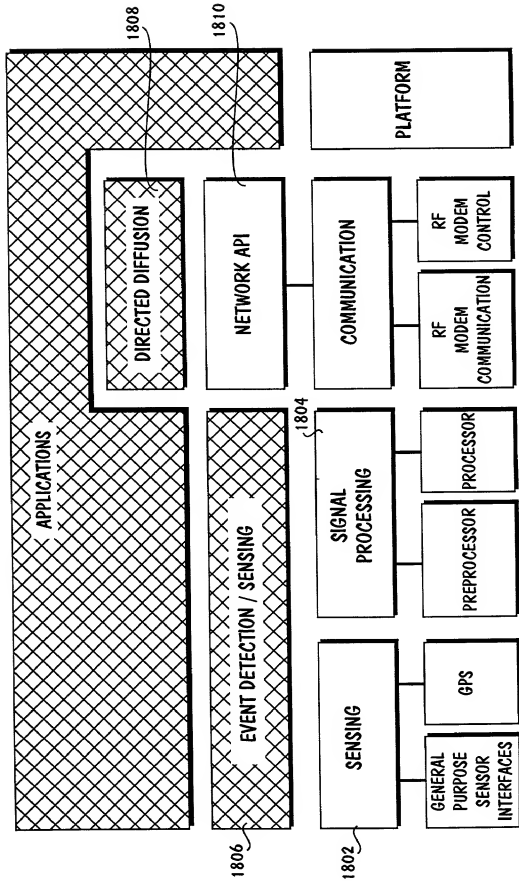


FIG. 18

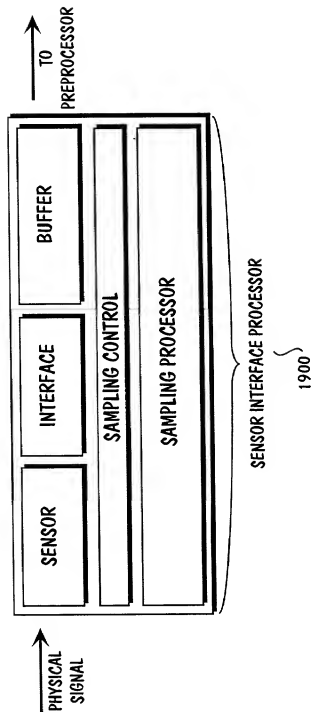


FIG. 19

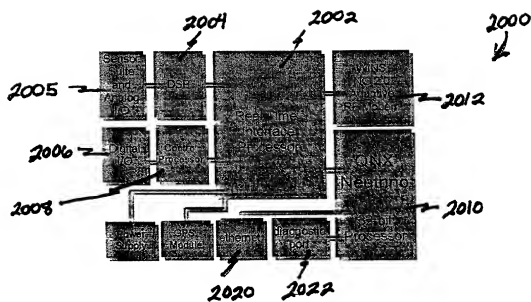
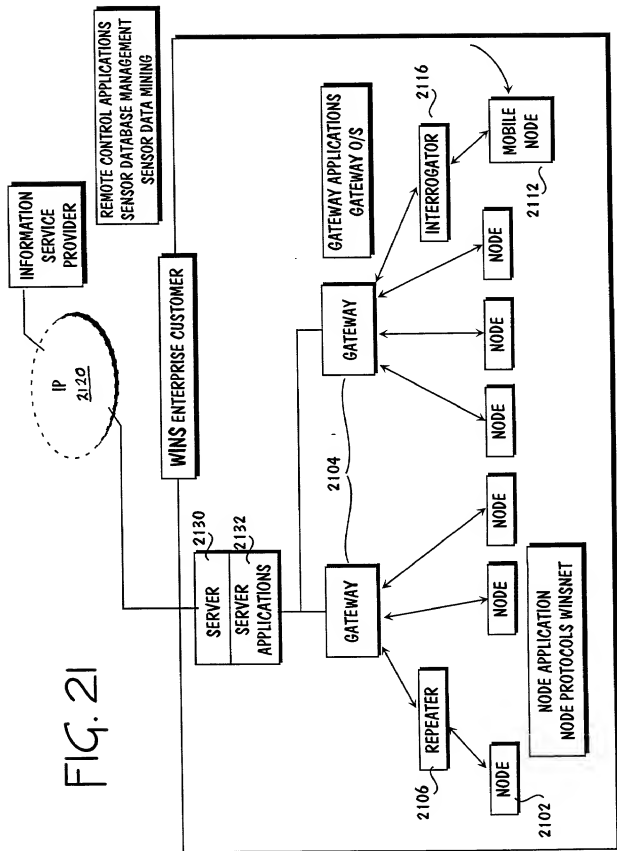


FIGURE 20

FIG. 21



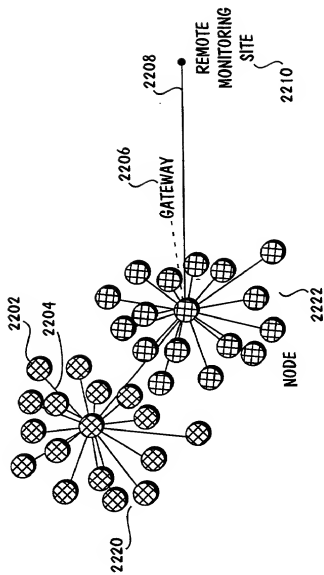


FIG. 22

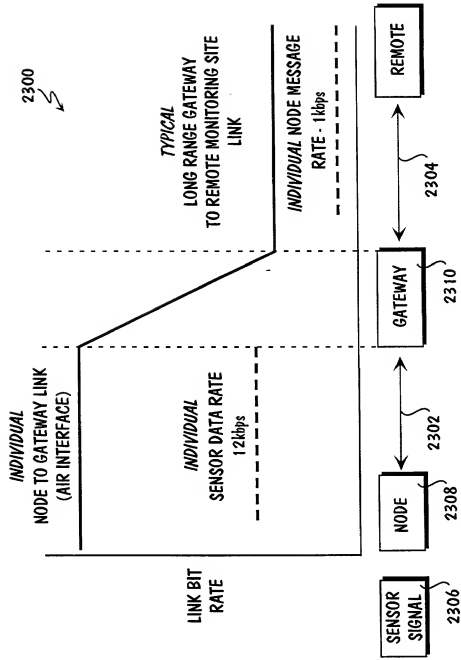


FIG. 23

00400F'90448960

2400



LENGTH (LOW ORDER BYTE)	LENGTH (HIGH ORDER BYTE)	DESTINATION ADDRESS	SOURCE ADDRESS	DATA (UP TO 2048 BYTES)	CHECKSUM
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FIG. 24



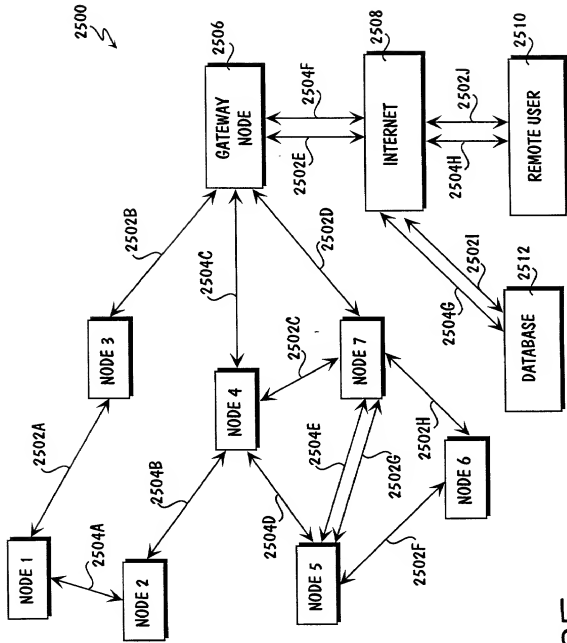


FIG. 25

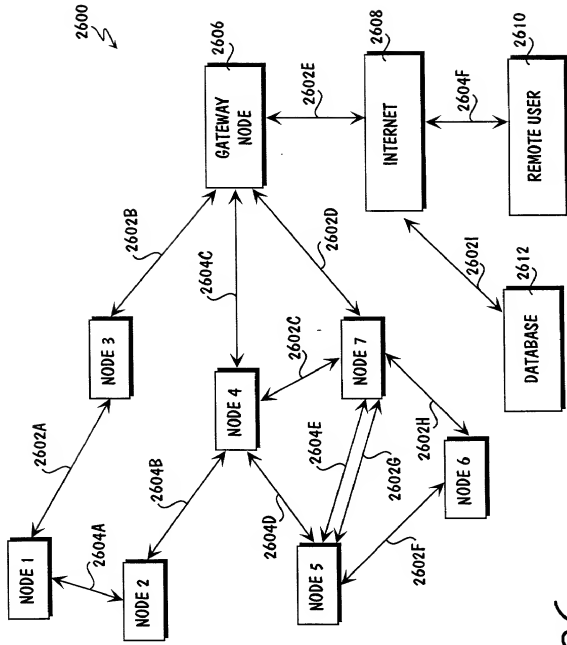
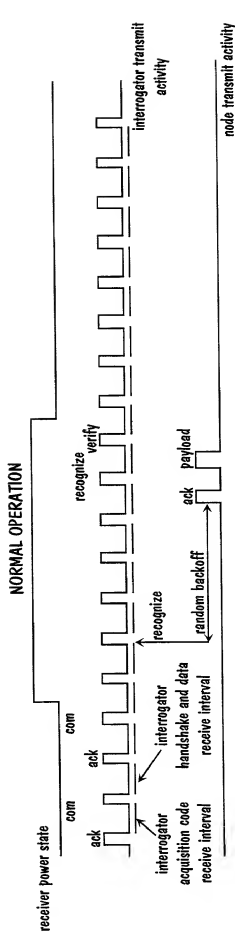


FIG. 26



**NORMAL OPERATION WITH NODE TRANSMIT DATA ERROR RECOVERY**

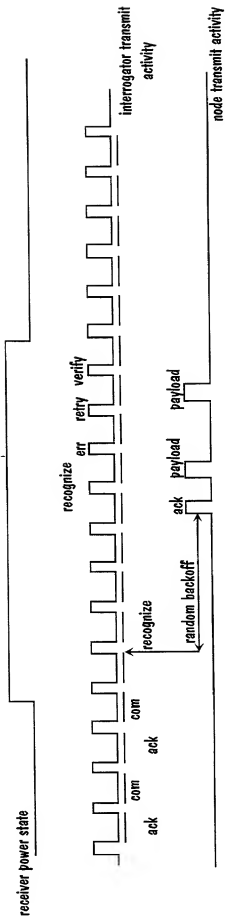


FIG. 27



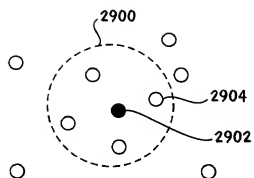


FIG. 29

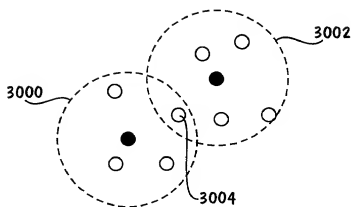


FIG. 30

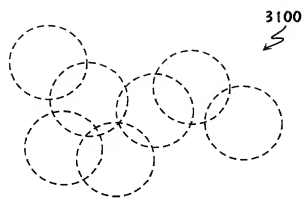


FIG. 31

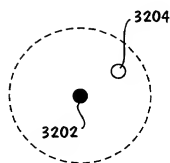


FIG. 32

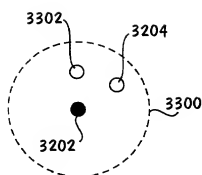


FIG. 33

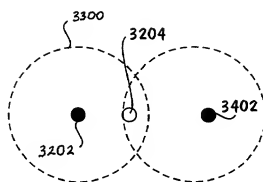


FIG. 34

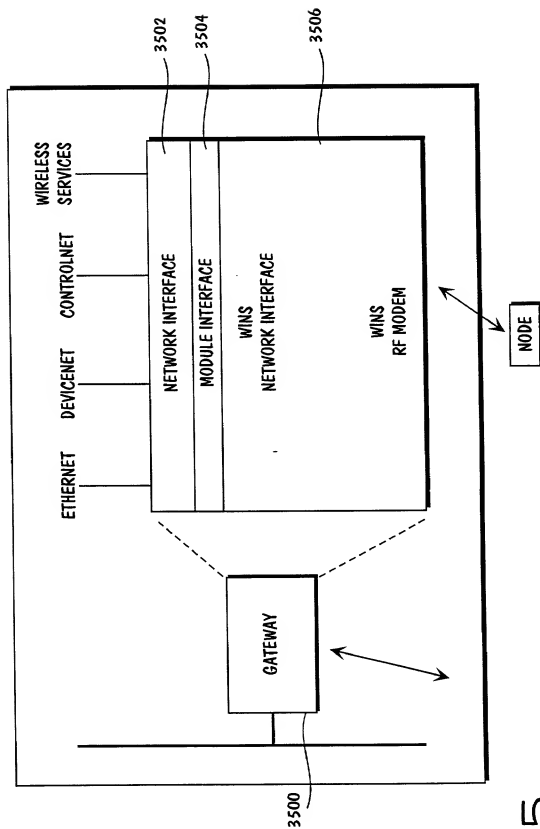


FIG. 35



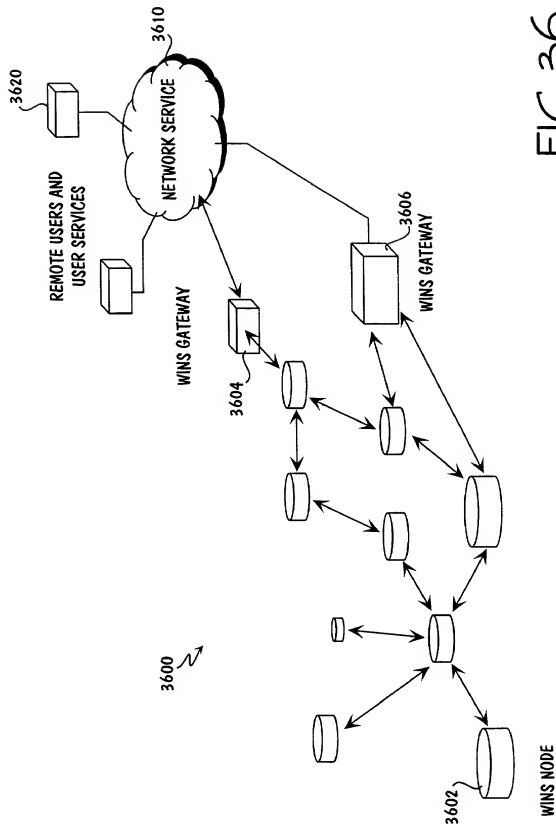


FIG. 36

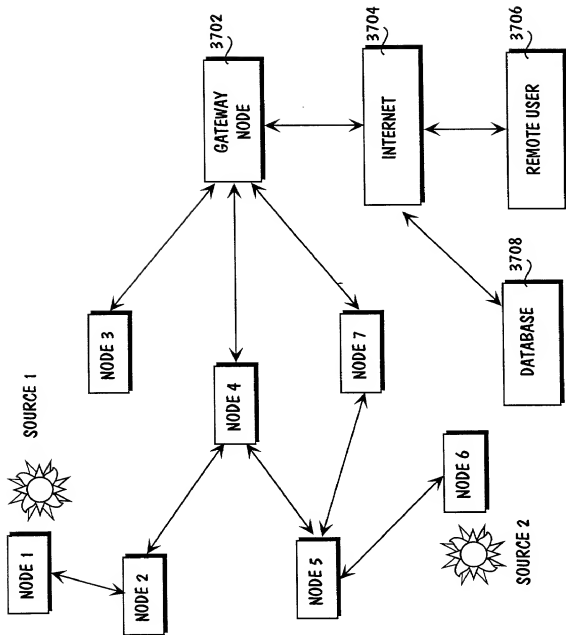


FIG. 37

```

graph TD
    3802[COLLECT RAW DATA AND PLACE  
IN SHORT TERM QUEUE IN NODE] --> 3804{ABOVE  
THRESHOLD  
?}
    3804 -- N --> 3806[DISCARD RECORD]
    3804 -- Y --> 3808[PERFORM FIRST LEVEL  
CLASSIFICATION]
    3808 --> 3810{ABOVE  
THRESHOLD  
?}
    3810 -- N --> 3812[STORE FOR INTERVAL  
TO ALLOW RESPONSE  
TO NEIGHBORS]
    3810 -- Y --> 3814[SOLICIT DATA AND PERFORM  
CLASSIFICATION]
    3814 --> 3816{RESULT  
ANOMALOUS  
?}
    3816 -- Y --> 3828[LONG-TERM STORAGE  
OR DISCARD]
    3816 -- N --> 3818[QUEUE RESULT AND LAUNCH  
MESSAGE TO END USER USING  
COMPACT REPRESENTATION]
    3818 --> A((A))
  
```

FIG. 38A

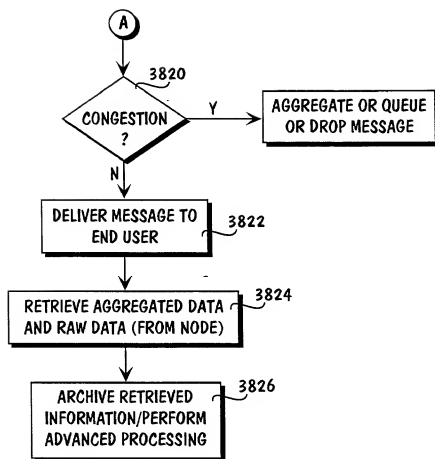


FIG. 38B

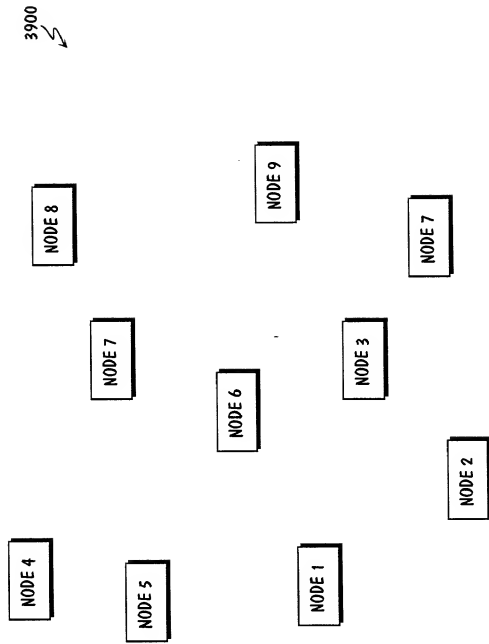


FIG. 39

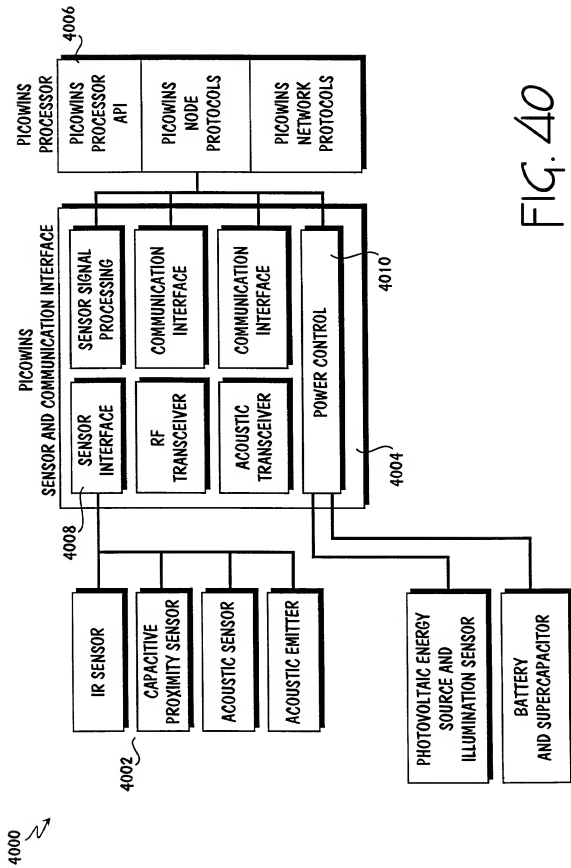


FIG. 40

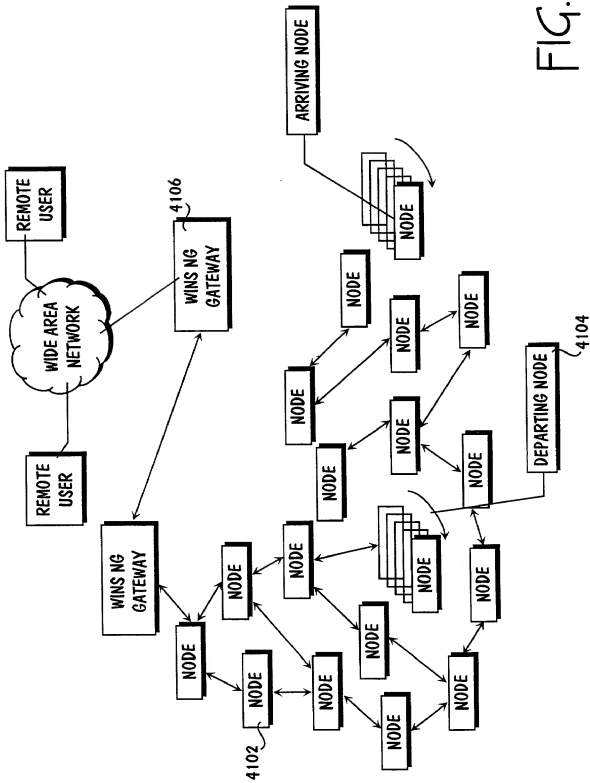


FIG. 41

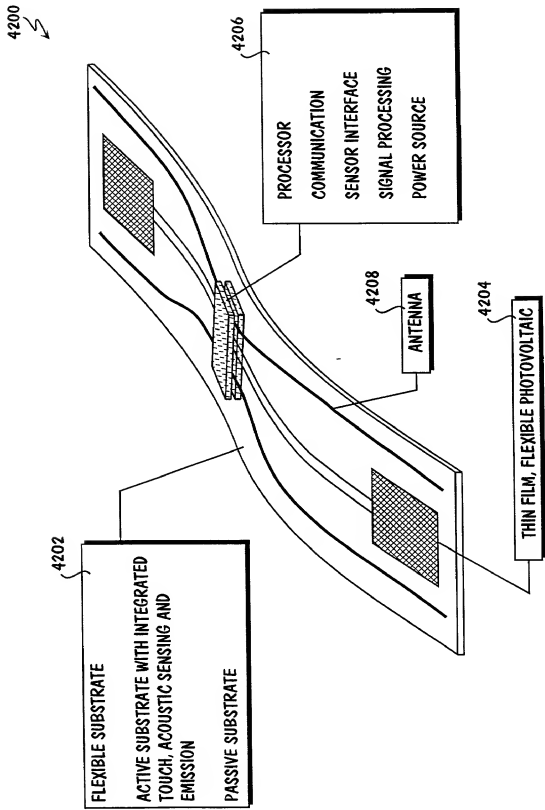


FIG. 42



Integrated sensors:

3-axis MEMS

accelerometer

Inductive sensor

acoustic, proximity,

etc..

4310

Processor:

4308

Communication

Sensor Interface

Signal Processing

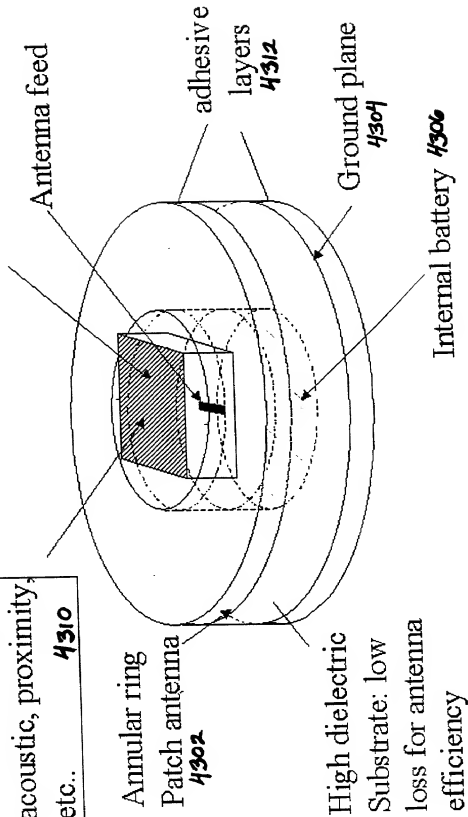


Figure 43

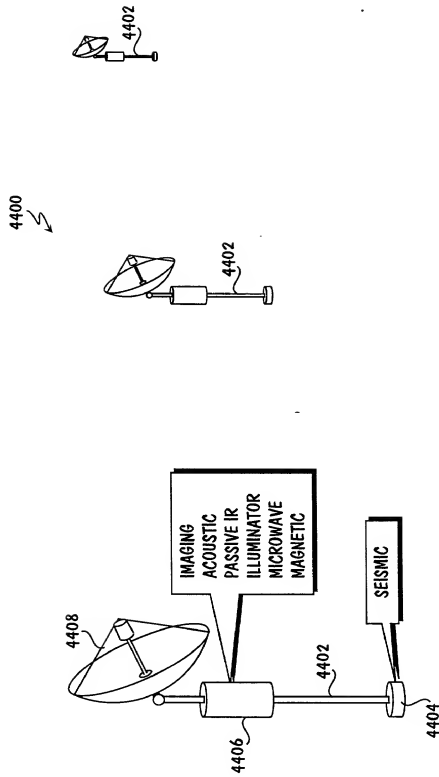


FIG. 44

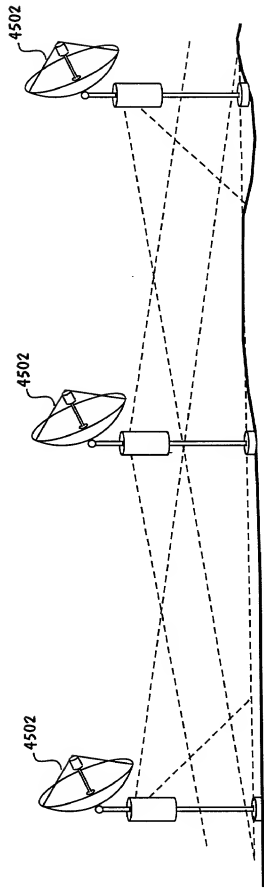


FIG. 45

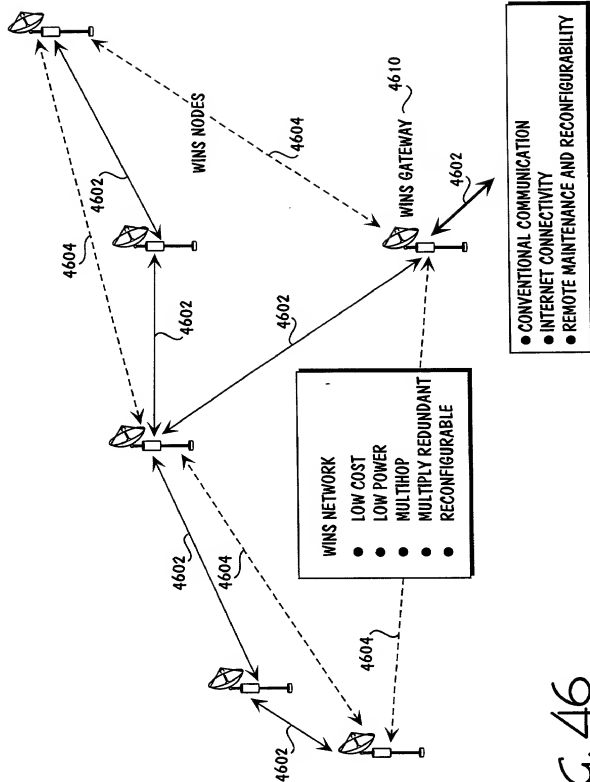


FIG. 46

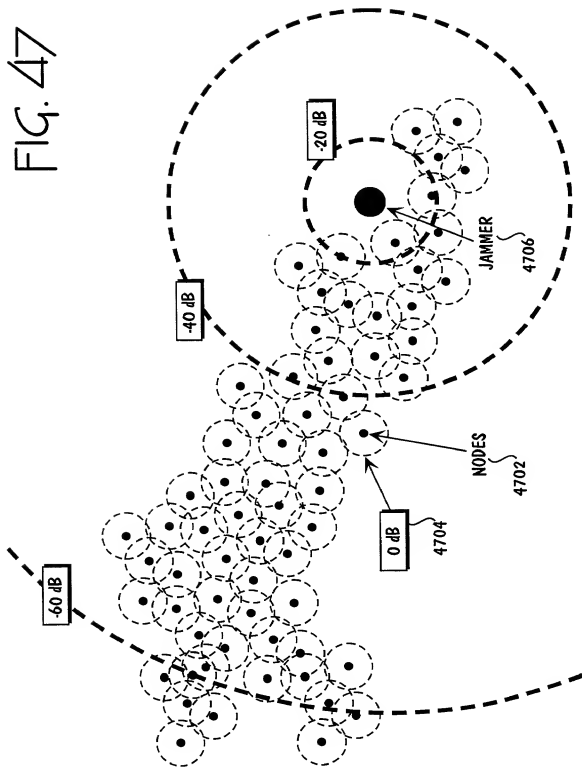
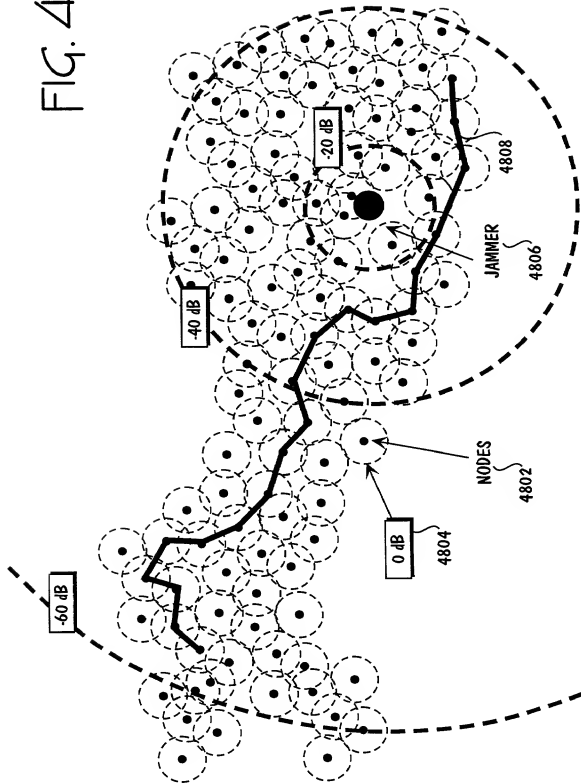


FIG. 48



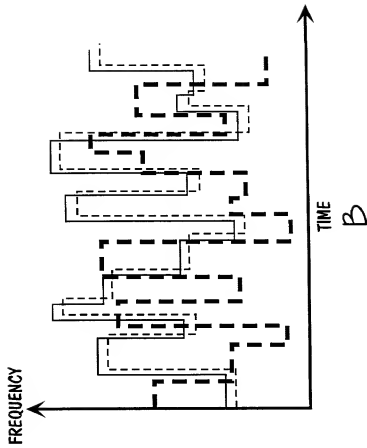
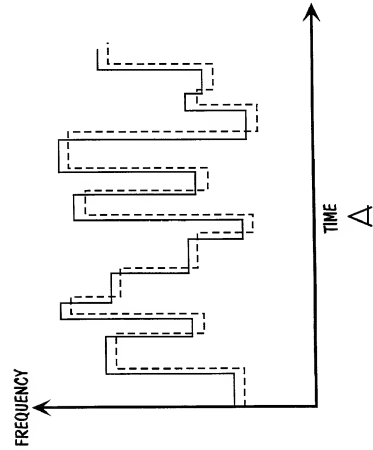


FIG. 49

# WINS ASSET MANAGEMENT ARCHITECTURE

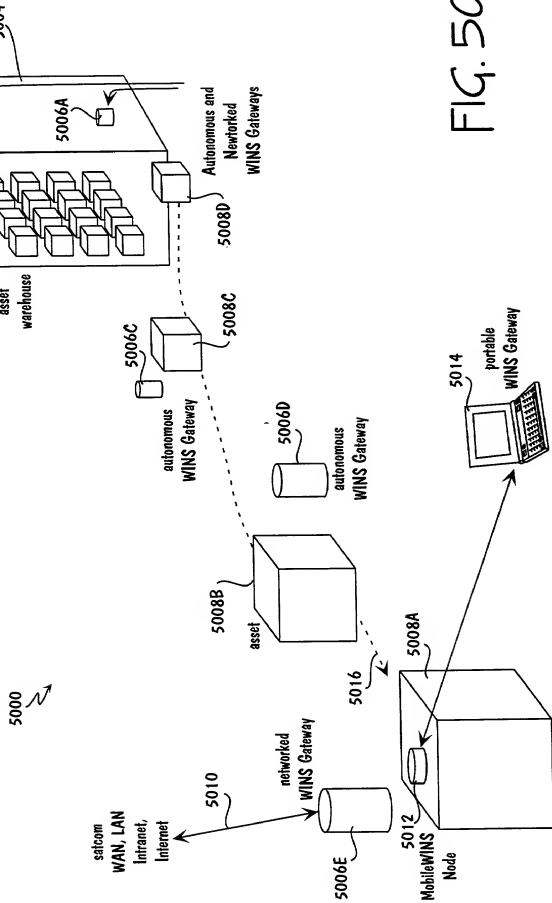


FIG. 50



004001-90268560

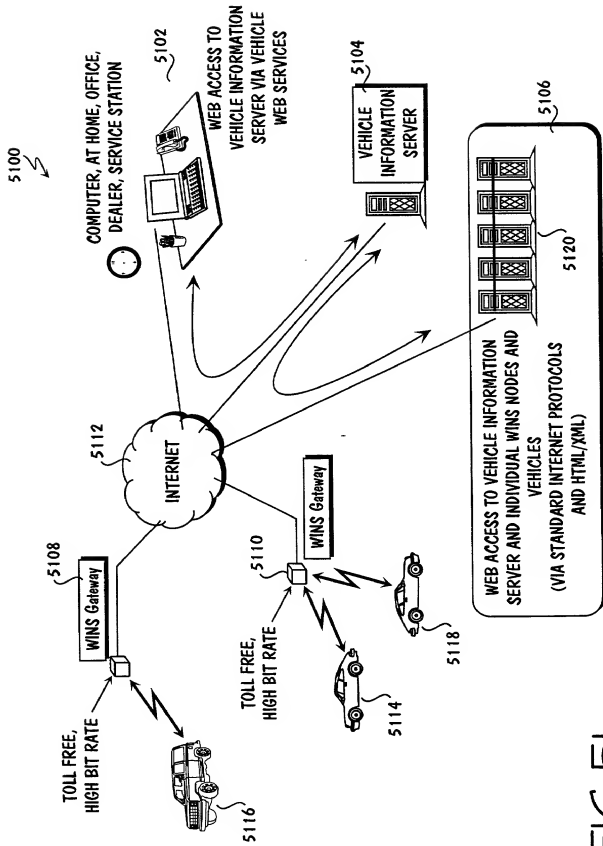


FIG. 51

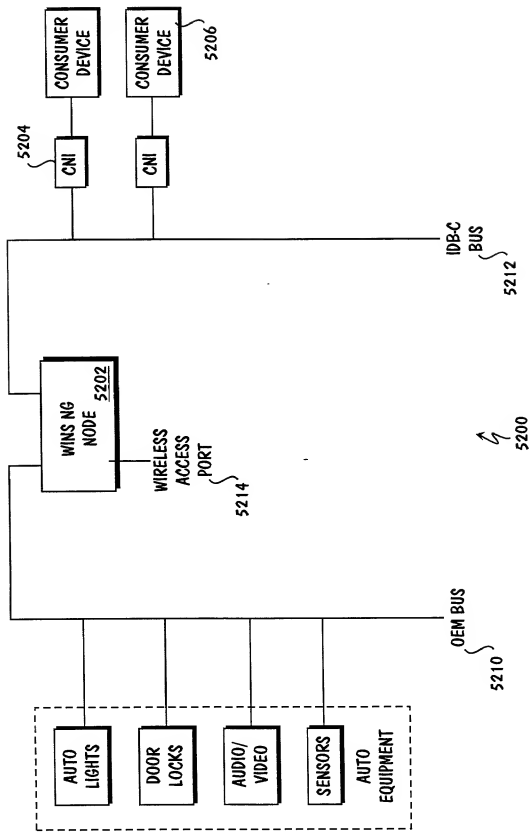


FIG. 52